

Fig. 1
(Prior Art)

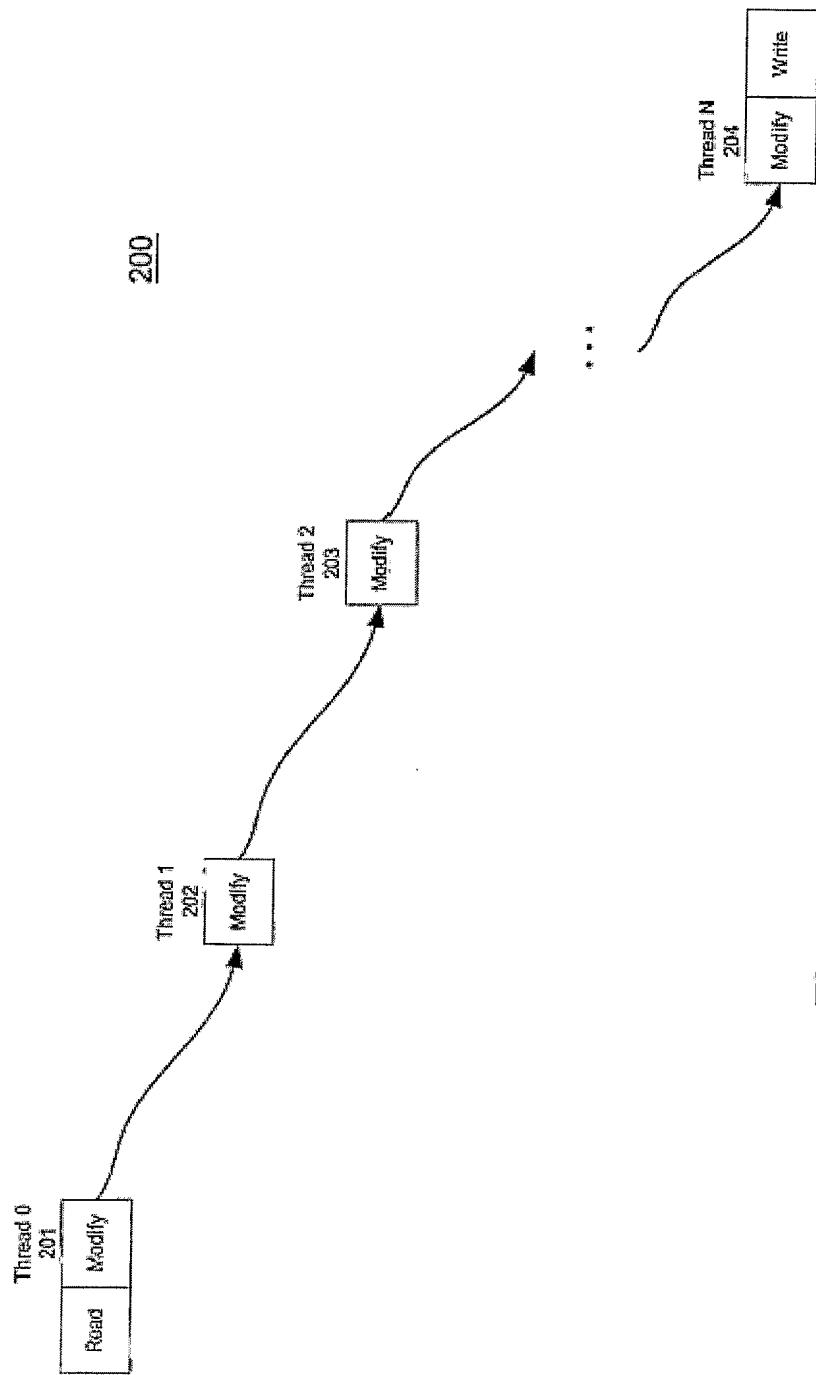


Fig. 2

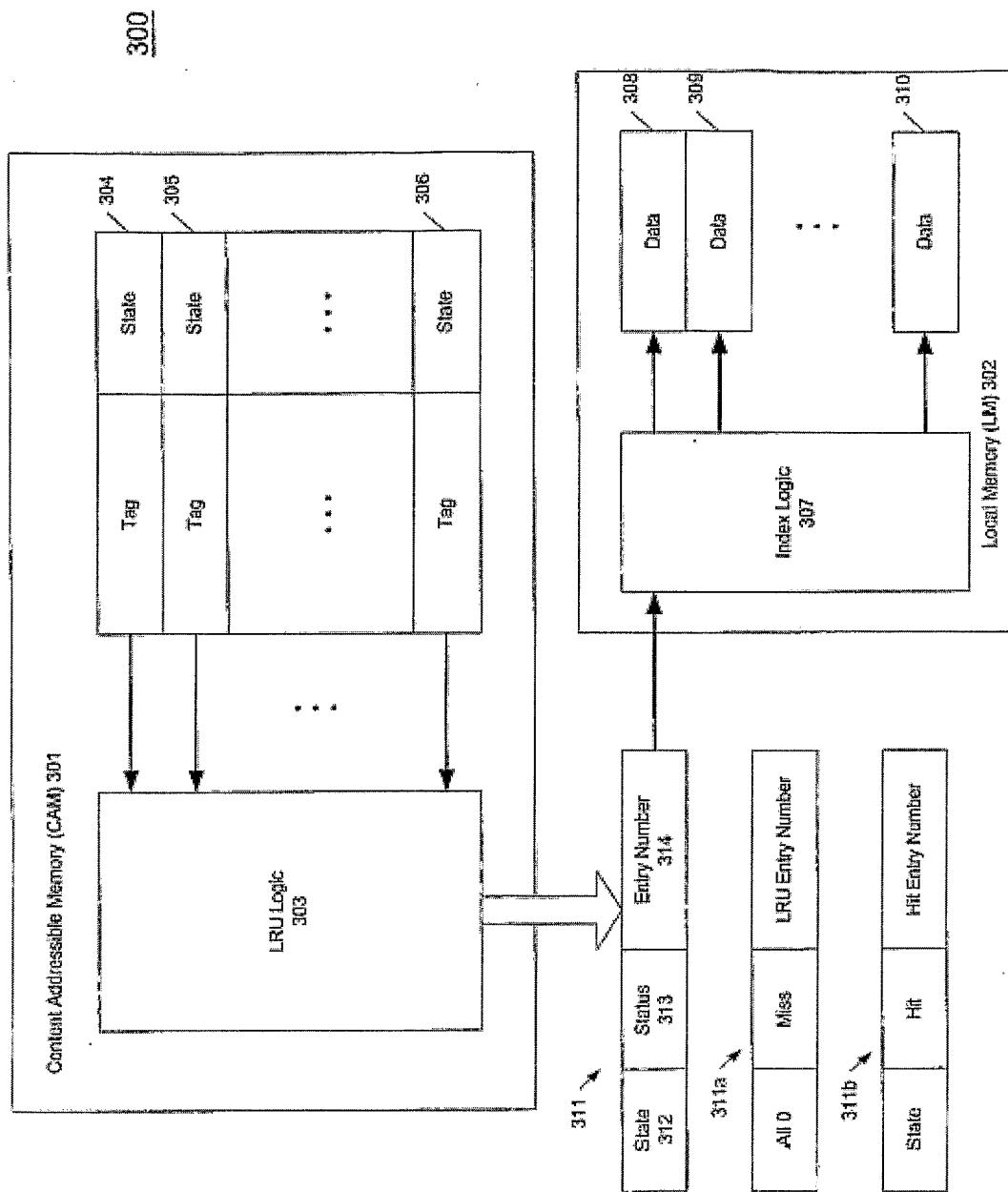
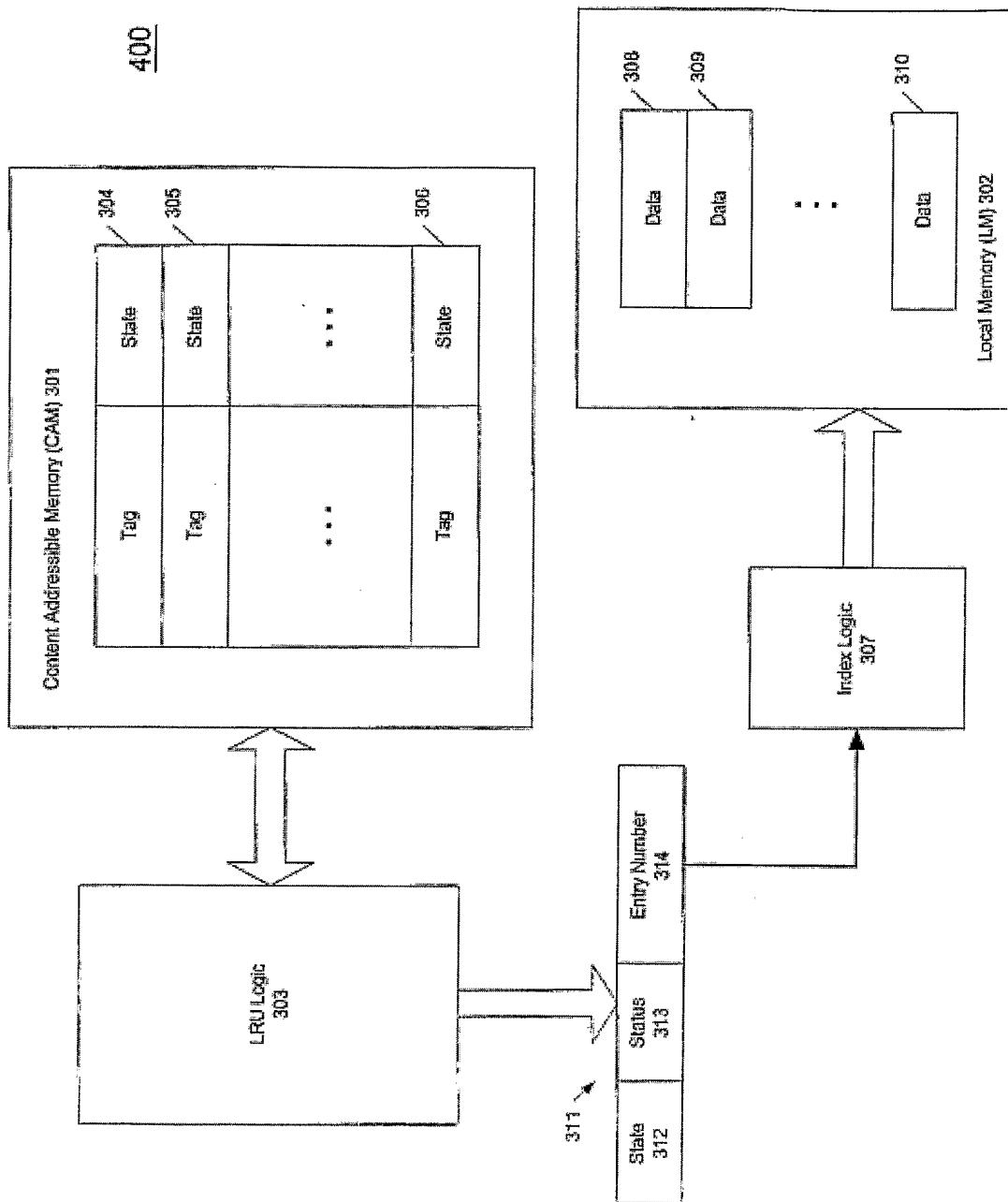
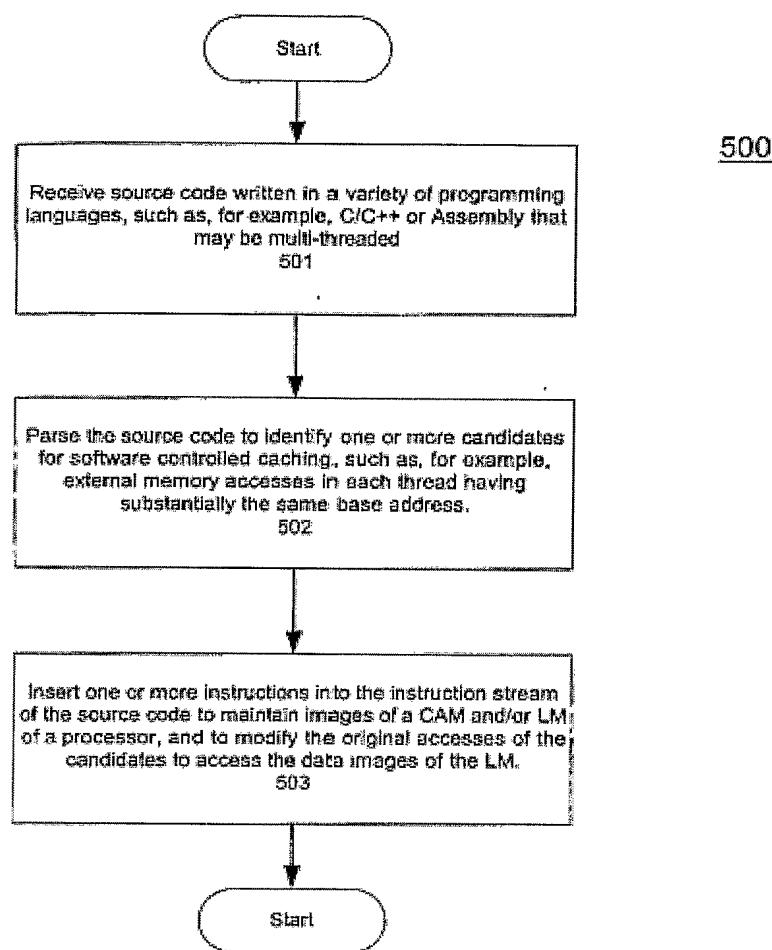
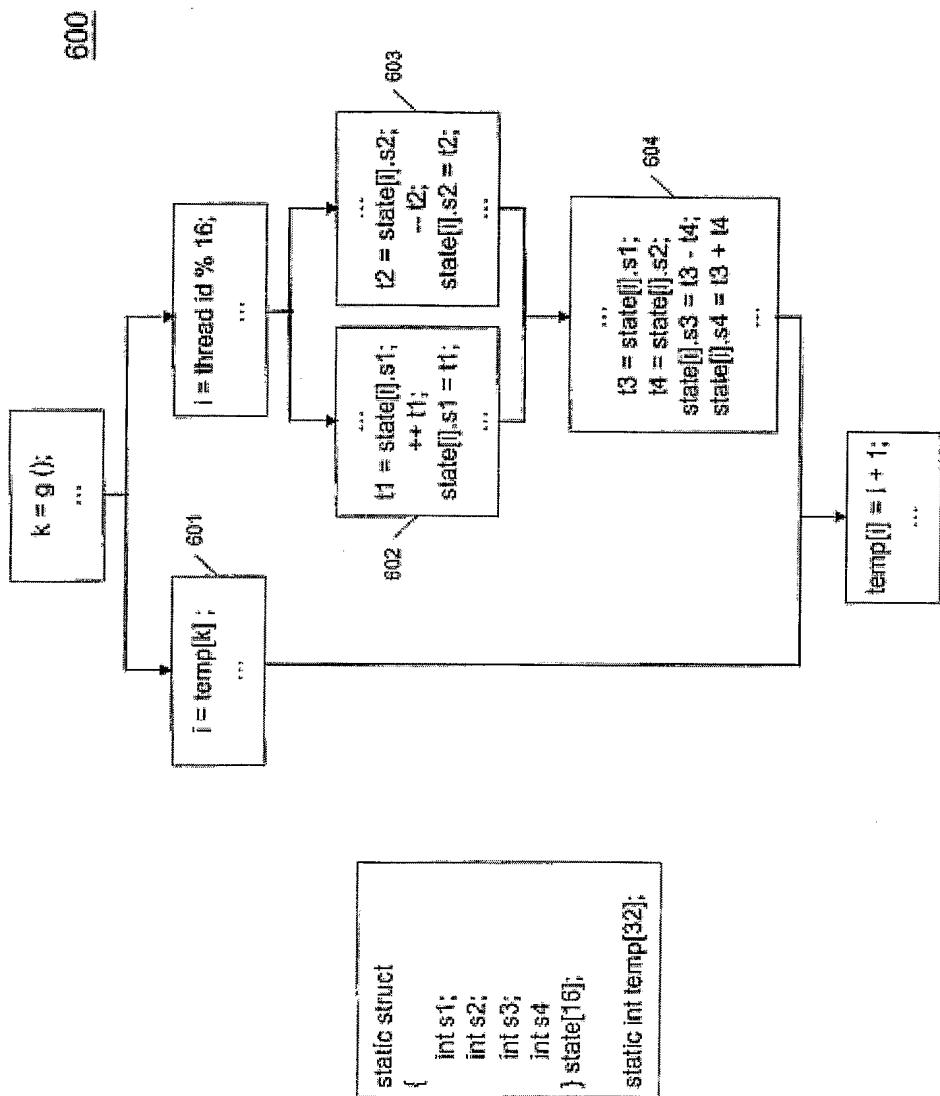


Fig. 3

**Fig. 4**

**Fig. 5**



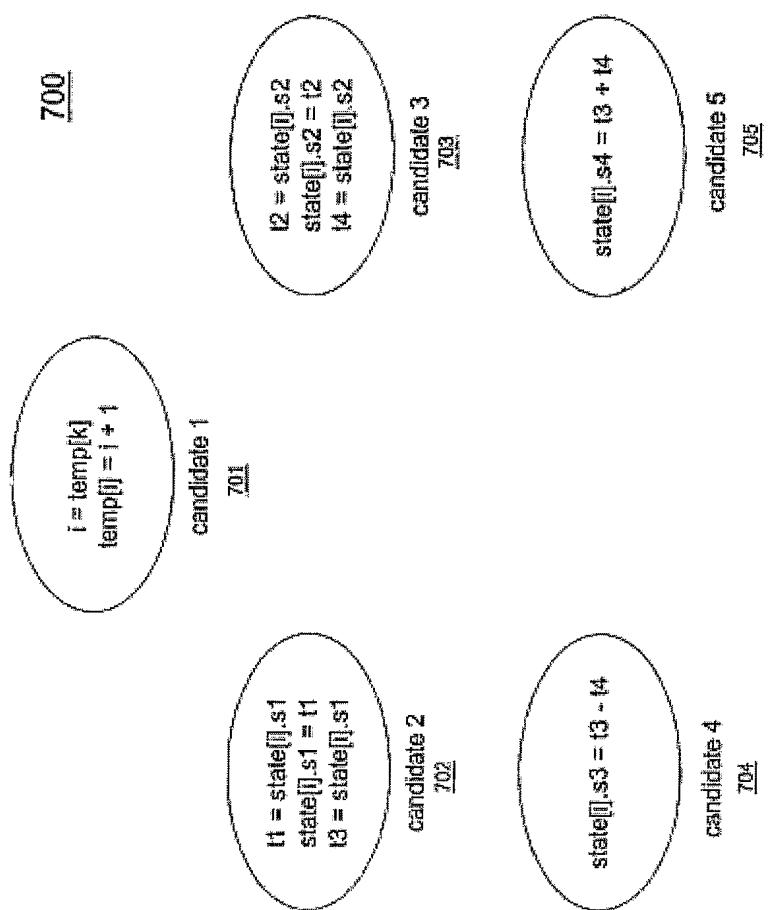
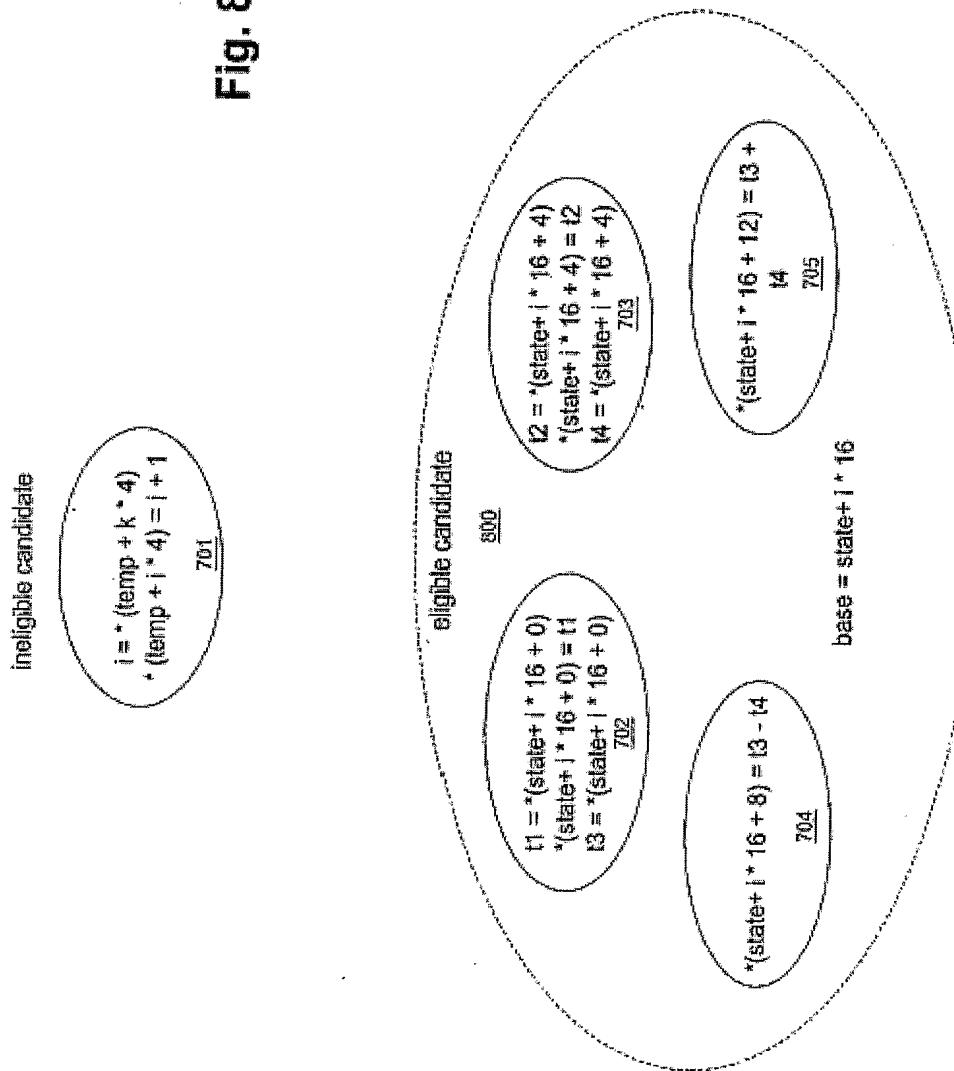
**Fig. 7**

Fig. 8



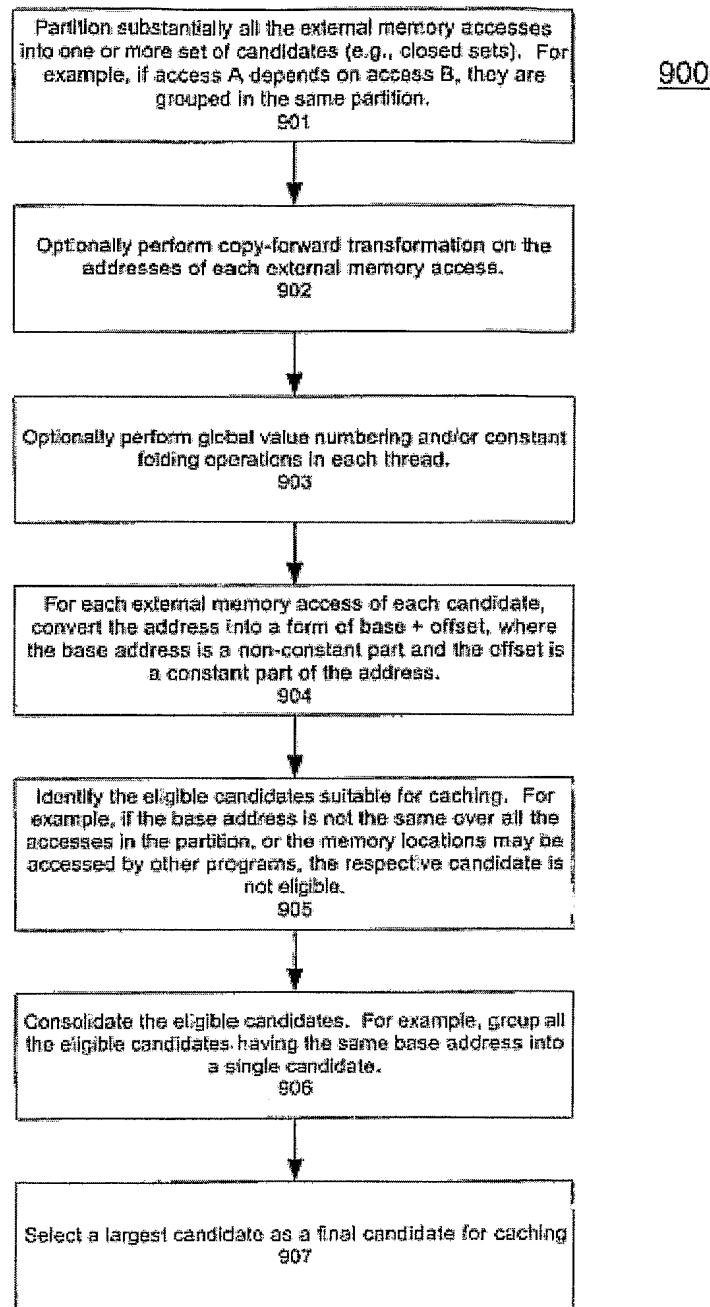
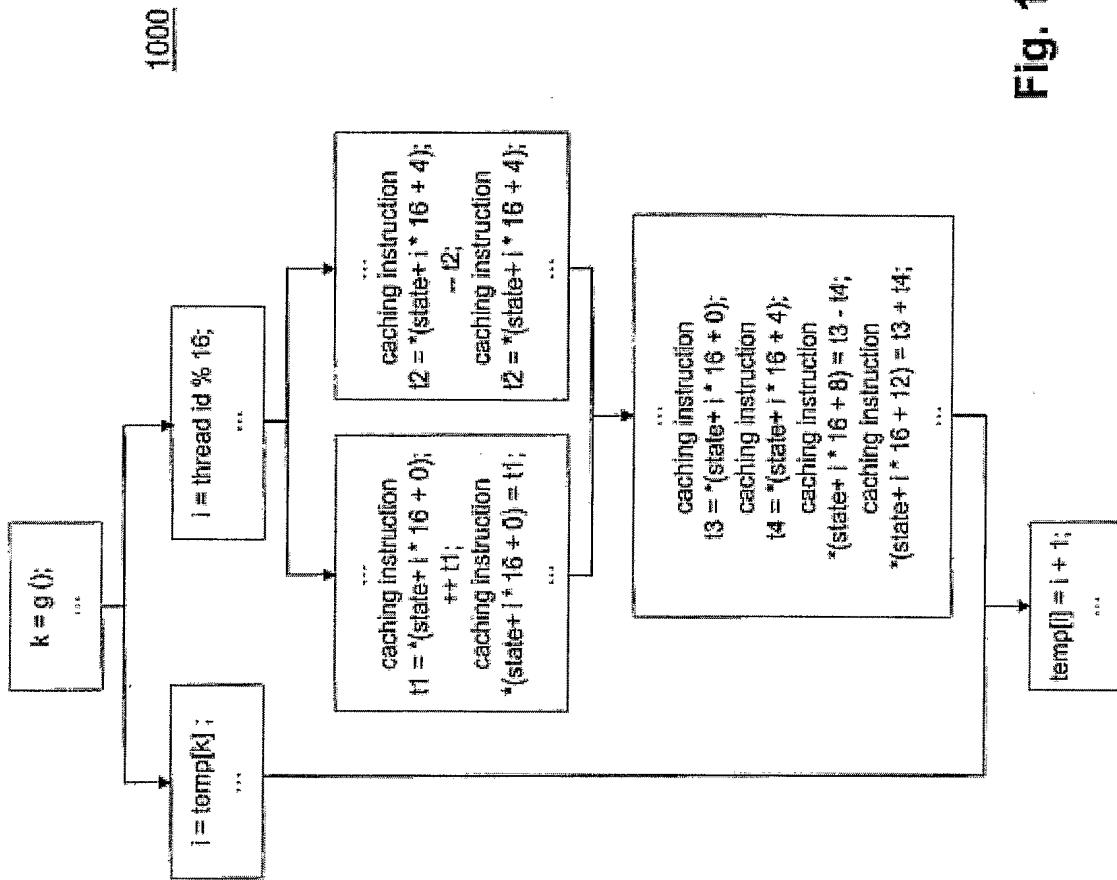


Fig. 9



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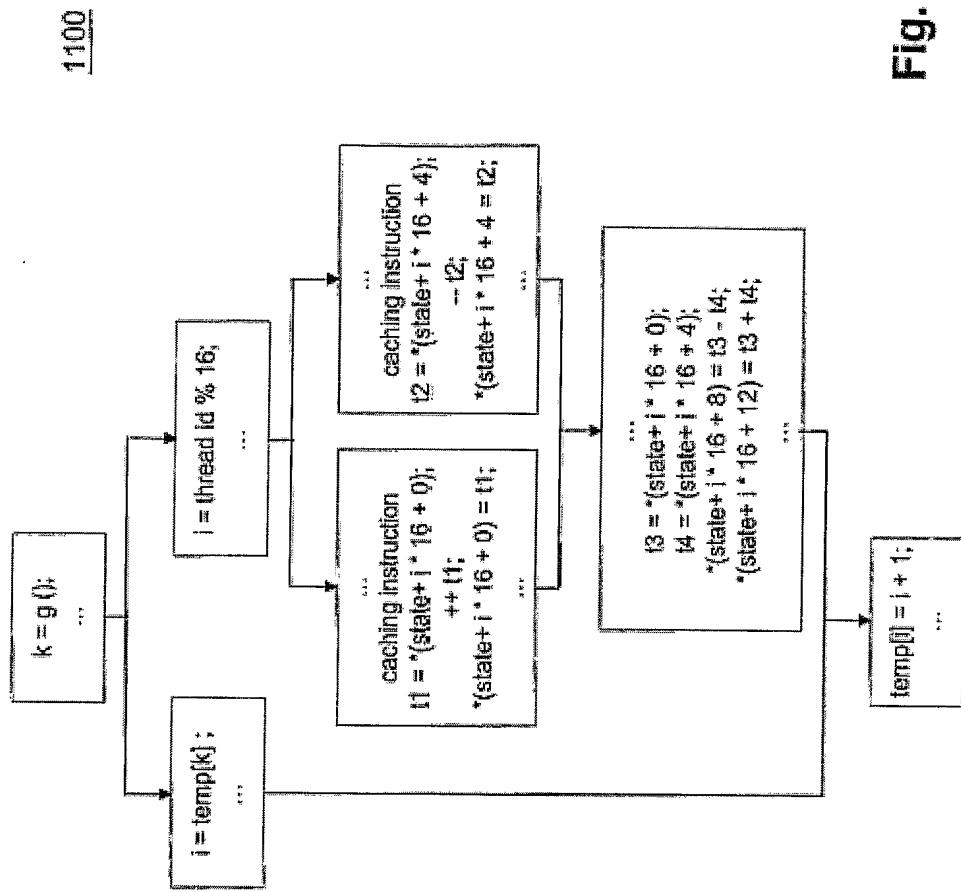


Fig. 11

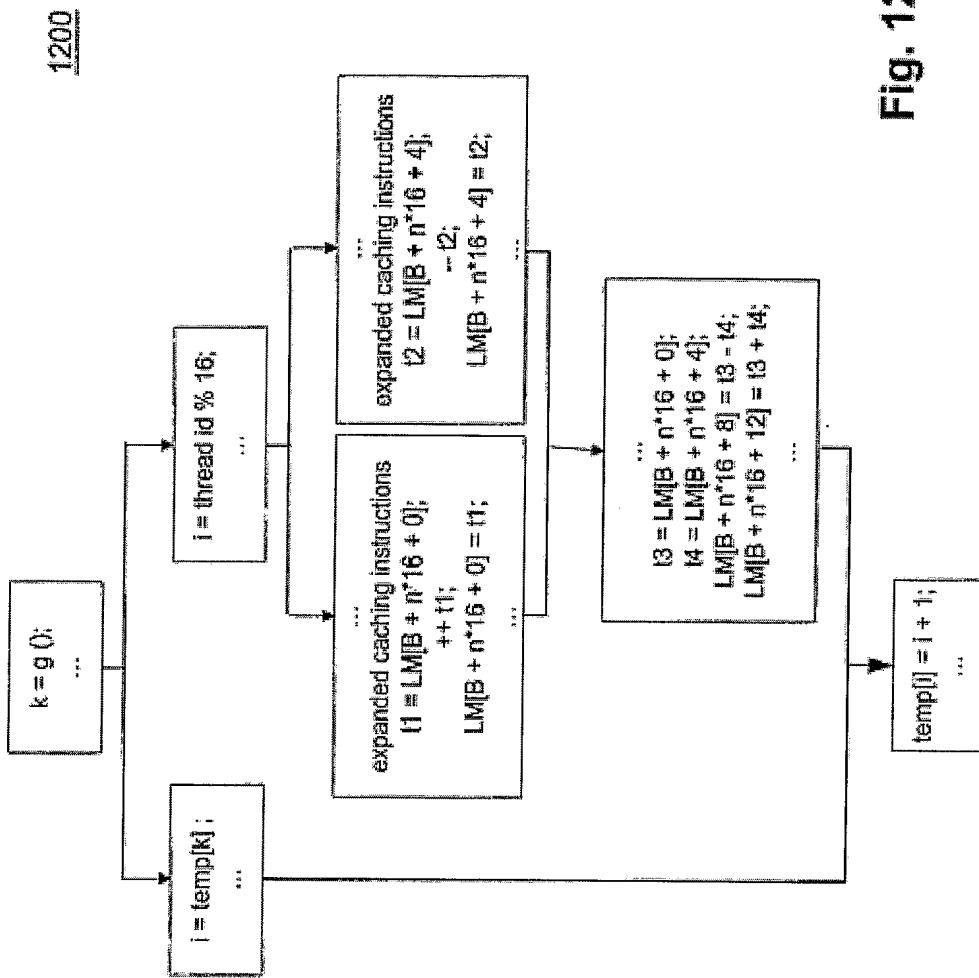
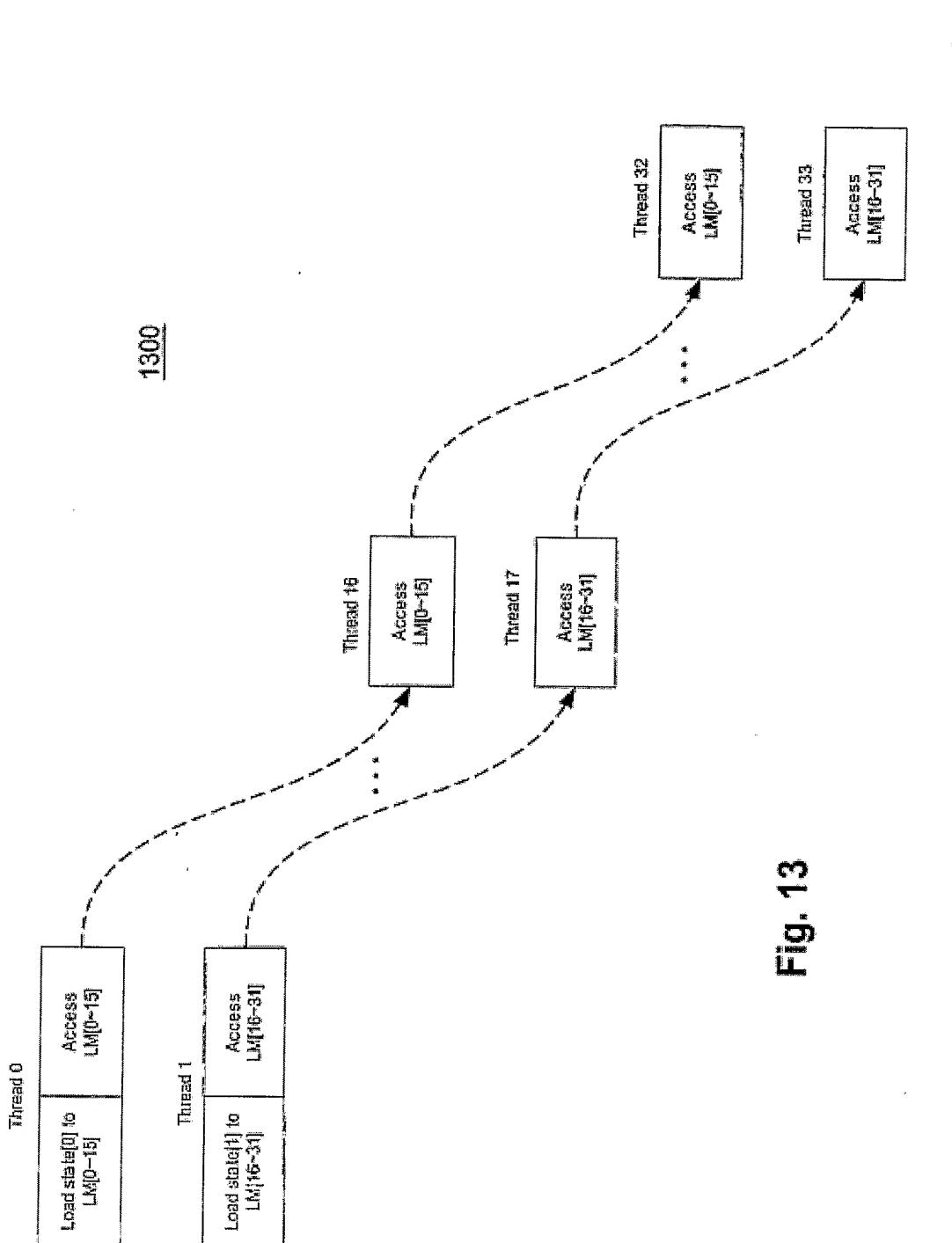


Fig. 12

**Fig. 13**

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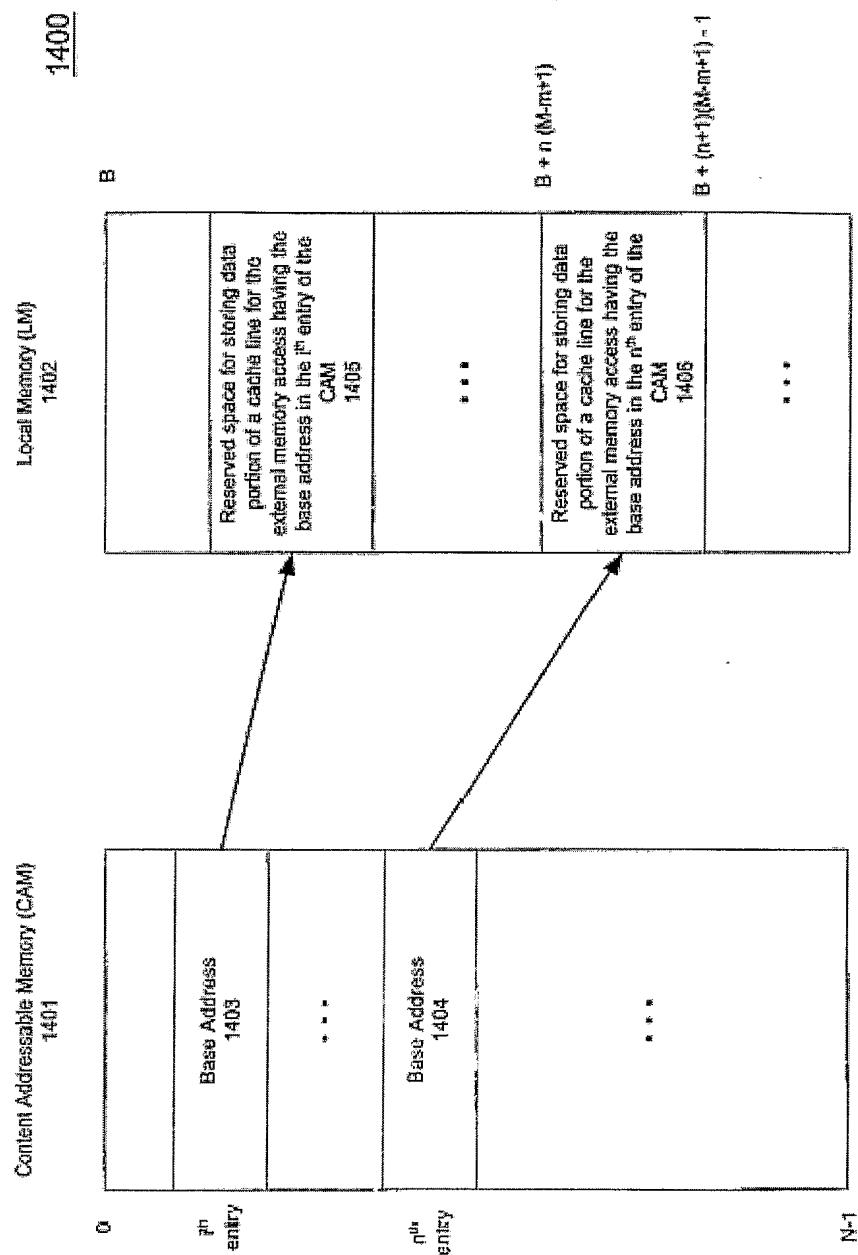
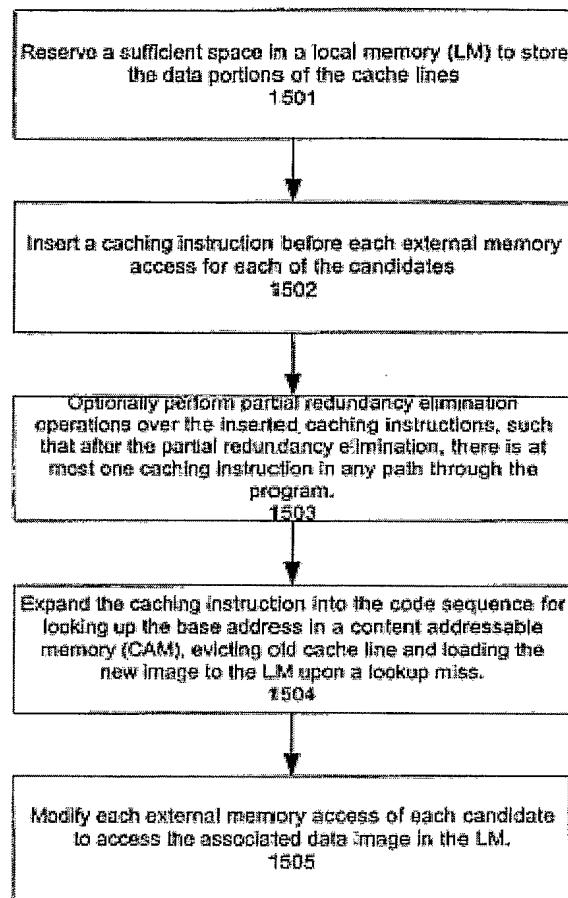


Fig. 14

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1500**Fig. 15**

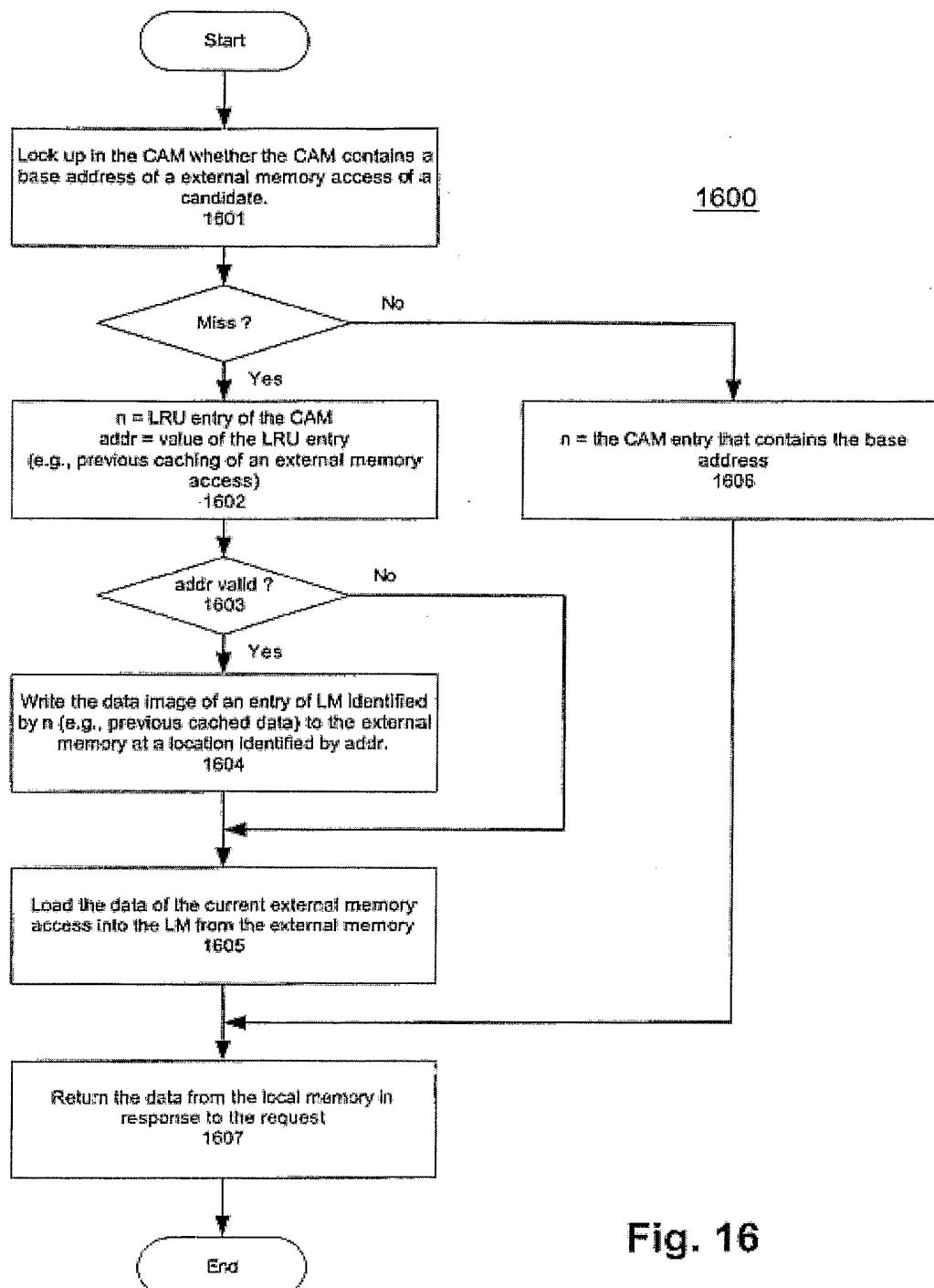


Fig. 16

```
CAM lookup for base
if (lookup miss)
{
    n = the LRU entry in CAM;
    addr = the value contained in the LRU entry in CAM;
    if (addr is valid)
    {
        write the data image in LM (from B + n * (M-m+1) to
        B + (n+1) * (M-m+1) -1) back to external memory
        (from addr + m to addr + M);
    }
    write base to the nth entry in the CAM;
    load external memory (from base + m to base + M)
    to LM (from B + n * (M-m+1) to B + (n+1) * (M-m+1) -1);
}
else
{
    n = the associated entry in CAM containing base;
}
```

Fig. 17

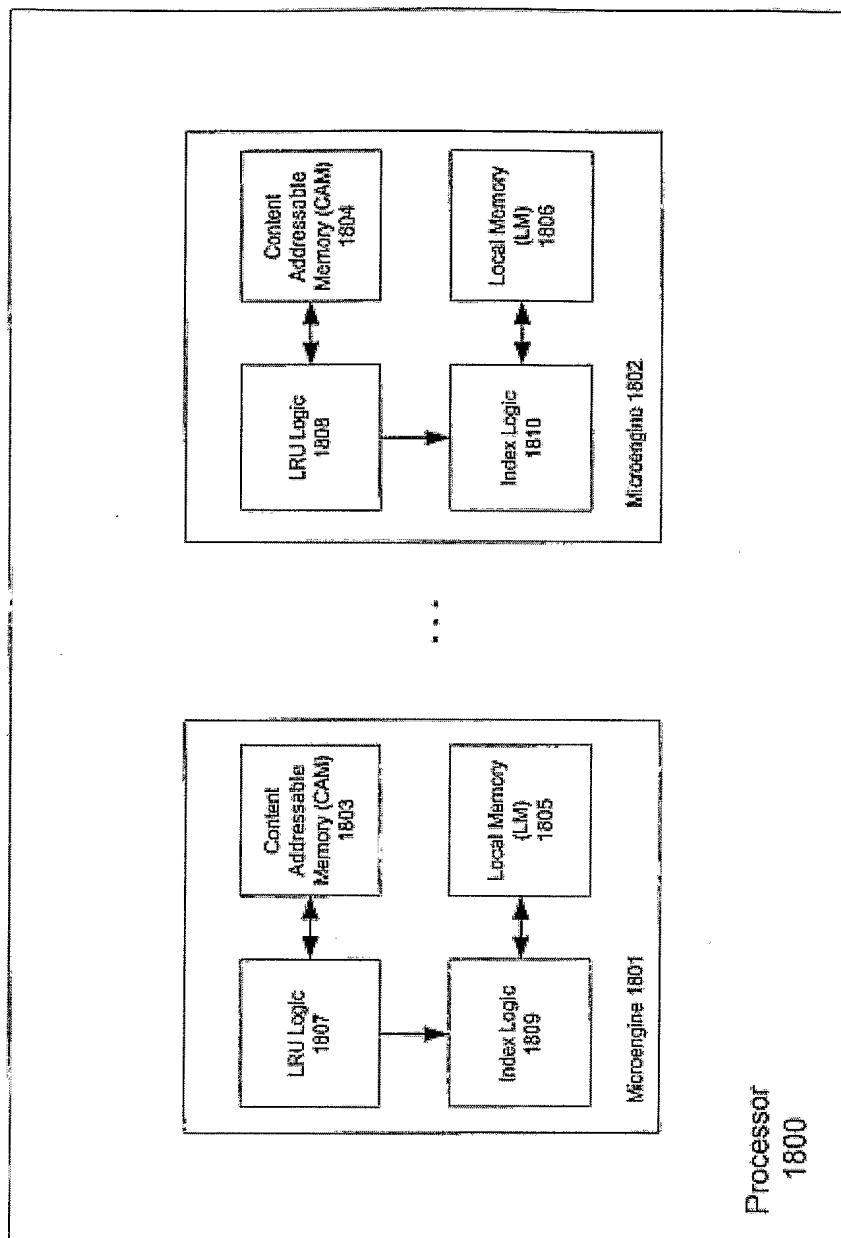


Fig. 18

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Fig. 19

